795	Viterbi decoding
796	Branch metric calculation
797	Majority decision/voter circuit
798	. Error detection for synchronization control
799	. Error/fault detection technique
800	Parity bit
801	Parity generator or checker circuit detail
802	Even and odd parity
803	Parity prediction
804	Plural dimension parity check
805	Storage accessing (e.g., address parity check)
806	Constant-ratio code (m/n)
807	Check character
808	Modulo-n residue check character
809	Code constraint monitored
810	Multilevel coding (n>2)
811	Forbidden combination or improper condition
812	Specified digital signal or pulse count
813	Two key-down detector
814	Data timing/clocking
815	Time delay/interval monitored
816	Two-rail logic
<u>817</u>	Noise level
<u>818</u>	Missing-bit/drop-out detection
<u>819</u>	Comparison of data
<u>820</u>	Plural parallel devices of channels
<u>821</u>	Transmission facility
822	Sequential repetition
823	True and complement data
<u>824</u>	Device output compared to input
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FOREIGN ART COLLECTIONS

FOR000 CLASS-RELATED FOREIGN DOCUMENTS

Any foreign patents or non-patent literature from subclasses that have been reclassified have been transferred directly to FOR Collections listed below. These Collections contain ONLY foreign patents or non-patent literature. The parenthetical references in the Collection titles refer to the abolished subclasses from which these Collections were derived.

MEMORY TESTING (371/21.1) DIGITAL LOGIC TESTING (371/22.1) DIGITAL DATA ERROR CORRECTION (371/30) FOR100 Scan path testing (LSSD) (371/22.3) FOR101 Including test pattern generator (371/27) FOR102 Block code (371/37.1) FOR103 Memory access (371/40.1) FOR104 Convolutional code (371/43) FOR288 ERROR/FAULT ANTICIPATION (371/4)

. Replacement with spare device or system (371/8.1)

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L Number	Hits	Search Text	DB	Time stamp
1	4	("6378125" "6230119" "5838975"	USPAT;	2004/01/20 13:58
-]	"6378124") and switch	US-PGPUB;	
1		•	EPO; JPO;	
			DERWENT;	
			IBM_TDB	0004/65/55
2	1	717/129.ccls. and context with switch	USPAT;	2004/01/20 13:59
			US-PGPUB;	
			EPO; JPO;	
			DERWENT; IBM TDB	
,	120	717/\$.ccls. and context with switch	USPAT;	2004/01/20 13:59
3	120	/17/5.CCIS. and Context with Switch	US-PGPUB;	2004/01/20 13:33
			EPO; JPO;	
		•	DERWENT;	
			IBM_TDB	
4	108	717/\$.ccls. and context with switch and	USPĀT;	2004/01/20 13:59
		processor	US-PGPUB;	į
			EPO; JPO;	
			DERWENT;	
_			IBM_TDB	2004/01/20 12 52
5	13	717/\$.ccls. and context with switch and	USPAT;	2004/01/20 13:59
		processor same breakpoint	US-PGPUB; EPO; JPO;	
			DERWENT;	
			IBM TDB	-
_	284	717/124.ccls.	USPAT;	2004/01/14 17:19
	204	111/121.0013.	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	5624	717/\$.ccls.	USPĀT;	2004/01/14 17:18
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
	0	717/104 cala and (multi add throads)th	IBM_TDB USPAT;	2004/01/14 17:20
-		717/124.ccls. and (multi adj thread\$4 with processor) same breakpoint\$4	US-PGPUB;	2007/01/17 1/.20
			EPO; JPO;	
			DERWENT;	
			IBM TDB	
_	0	· = · · = = · · = = · · · · · · · · ·	USPAT;	2004/01/14 17:20
		processor) same (break adj point\$4)	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
	_	217/104 3 (3/11 3/11	IBM_TDB	2004/01/14 17:01
-	1	717/124.ccls. and (multi adj thread\$4 with	USPĀT; US-PGPUB;	2004/01/14 17:21
		processor)	EPO; JPO;	
			DERWENT;	. *
	ļ		IBM TDB	
_	0	717/124.ccls. and (multi adj thread\$4 with	USPAT;	2004/01/14 17:21
	1	processor) same debug	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	0	717/\$.ccls. and (multi adj thread\$4 with	USPAT;	2004/01/14 17:22
		processor) same debug	US-PGPUB;	
			EPO; JPO; DERWENT;	
			IBM TDB	
_	17	717/\$.ccls. and (break adj point) same	USPAT;	2004/01/14 17:24
	'	processor	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
	1		IBM_TDB	
-	2	717/\$.ccls. and (break adj point) same	USPAT;	2004/01/14 17:37
		processor and (multi adj thread\$4)	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
L	<u> </u>		IBM TDB	<u> </u>



-	0	717/\$.ccls. and (break adj point) same	USPAT;	2004/01/14 17:38
		processor and (multi adj thread\$4) and	US-PGPUB;	
		(micro adj engin\$4)	EPO; JPO;	
			DERWENT;	
			IBM_TDB	0004/01/14 17:30
-	0	(===== == <u>) </u>	USPAT;	2004/01/14 17:38
	1	(multi adj thread\$4) and (micro adj	US-PGPUB;	
		engin\$4)	EPO; JPO;	
			DERWENT;	
		l	IBM_TDB	0004/01/16 10 05
-	354	(break adj point) same count\$4	USPAT;	2004/01/16 12:35
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
	,	(harak adi paint) gama gayaté4 gama alu	IBM_TDB USPAT;	2004/01/16 12:35
-	4	(break adj point) same count\$4 same alu	US-PGPUB;	2004/01/10 12.33
			EPO; JPO;	
			DERWENT;	
			IBM TDB	
	2	(break adj point) same count\$4 same alu	USPAT;	2004/01/16 12:37
-		and library	US-PGPUB;	2004/01/10 12:57
		and library	EPO; JPO;	
			DERWENT;	
			IBM TDB	
_	3	(break adj point) same count\$4 same alu	USPAT;	2004/01/16 13:06
-	3	and (library or record)	US-PGPUB;	2004701710 13:00
		and (libiary of fecola)	EPO; JPO;	
			DERWENT;	
			IBM TDB	
_	0	(break adj point) same count\$4 same alu	USPĀT;	2004/01/16 12:51
		and (library or record) near (context with	US-PGPUB;	
		switch)	EPO; JPO;	
			DERWENT;	
			IBM TDB	
_	l 0	(break adj point) near (context with	USPĀT;	2004/01/16 12:51
	1	switch)	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
_	0	(break adj point) near (context with	USPAT;	2004/01/16 12:52
		switch\$4)	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	7411	context with switch\$4	USPAT;	2004/01/16 12:52
			US-PGPUB;	
			EPO; JPO;	
			DERWENT; IBM TDB	
	_	(contout with quitabed) noon (brook as-	USPAT;	2004/01/16 12:52
-	0	(context with switch\$4) near (break adj	USPAT; US-PGPUB;	2004/01/10 12:32
		horneda)	EPO; JPO;	
			DERWENT;	
			IBM TDB	
_	1	(break adj point) same count\$4 same alu	USPAT;	2004/01/16 13:11
		and (library or record) and processor same	US-PGPUB;	
		(multiple or many) with instruction\$4	EPO; JPO;	
		,	DERWENT;	
			IBM TDB	
_	15	("6378125" "6230119" "5838975"	USPĀT;	2004/01/16 14:52
		"6378124") and processor same (multiple	US-PGPUB;	
		with instruction\$4 or program\$4)	EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	6	,	USPAT;	2004/01/20 13:56
		"6378124") and processor same (multiple	US-PGPUB;	
		with instruction\$4 or program\$4) and	EPO; JPO;	
		register	DERWENT;	
	L	<u> </u>	IBM_TDB	